

1 **ABSTRACT**

2 One or more mapping data structures are maintained containing mappings
3 of logical flash memory addresses to physical flash memory addresses. Each
4 mapping data structure has a predetermined capacity of mappings. A master data
5 structure is also maintained containing a pointer to each of the one or more
6 mapping data structures. Additional mapping data structures are allocated as
7 needed to provide capacity for additional mappings. Each time a mapping data
8 structure is allocated or de-allocated the pointers in the master data structure are
9 changed accordingly.

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